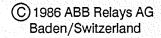
CH-ES 83-92.10 E Edition May 1991

ASEA BROWN BOVERI

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modures[®] Static Distance Relays Types LZ91, LZ92, LZ92-1

Instructions for Installation and Operation



4th edition

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List of abreviations and symbols

I _R , I _S , I _T	: phase currents
\mathtt{I}_{Σ}	: neutral current
I _B	: load current
I Bmax	: max. load current possible
I _A	: balancing current
I _N	: rated current
IK	: fault current
^U _R , ^U s, ^U _T	: phase-to-neutral voltages
$ \mathbf{k} e^{j\varphi \mathbf{k}}$ o	: ratio of zero to positive-sequence impedances (neutral current factor)
φ , φ_{L}	: phase-angle of line
UΔ	: difference voltage (difference between fault voltage and replica impedance voltage)
$^{\mathrm{U}}\Sigma$: sum voltage (sum of fault voltage and replica impedance voltage)
x _A , x _B	: forwards resp. reverse replica reactances of the underimpedance starters of LZ92 or LZ92-1
x _L	: reactance of line
^z i	: impedance of zone i
$Z_{L} = R_{L} + jX_{L}$: positive-sequence impedance of line
Z _{oL}	: zero-sequence impedance of line
Index i	: zone No.
Index p	: primary values
Index s	: secondary values

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1. APPLICATION

The solid-state distance relays LZ91, LZ92 and LZ92-1 have been designed for high-speed discriminative protection applications, primarily in medium-voltage systems. They are equally suitable for cable and overhead line circuits and the power systems may be ungrounded, impedance or solidly grounded.

When protecting extremely short lines, the distance relays are already equipped with the logic to operate in a directional comparison scheme (permissive overreaching transfer tripping) with pilot wires. The corresponding pilot wire ancillaries have the type designations ER91 and NR91.

The relays are also prepared for operating in conjunction with an auto-reclosure relay (e.g. type WT91 for three-phase reclosure).

All the relays are of the switched type, having starting units (fault detectors) which apply the correct fault quantities to a single direction and distance determining measuring system. The starting units of the LZ91 operate on overcurrent, so that the relay is simpler and more economical, but can only be applied in systems in which the lowest fault current is clearly greater than the maximum load current. Both the LZ92 and the LZ92-1 are equipped with true underimpedance units (not just voltage-controlled overcurrent) and are thus capable of detecting weak faults at times of low generation with fault currents below the maximum load currents at peak periods.

A full description of their operation is contained in publication CH-ES 23-92.10E "Solid-state distance relays types LZ91, LZ92 and LZ92-1" and their technical data is given in data Sheet CH-ES 63-92.10 E.

2. MECHANICAL DESIGN

The various plug-in units which make up the relays are accommodated in standard 19" electronic equipment racks, and these in turn are either grouped together with other relays in cubicles, or fitted into casings for surface or semiflush switchpanel mounting. The corresponding dimensioned drawings are in the appendices to these instructions.

The plug-in units have a standard height of 3,5 U (U = 44.45 mm) and varying widths given as a number of divisions T (T = 17 mm). One 19" rack has space for plug-in units adding up to a maximum of 24 T.

The standard versions of the distance relays LZ91, LZ92 and LZ92-1 only require 20 T, which is less than one full rack.

The distance relays comprise the following plug-in units:

- input transformer unit type EW91, width 6 T, also containing signal conditioning circuits, measuring sockets and auxiliary tripping relay.
- main processing unit: KI91 for LZ91, KZ91 for LZ92 or KZ91-1 for LZ92-1. The terminals of the main processing units are compatible and they have the same width.

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- signalling unit AV91 and/or AV94 (1 T).
- auxiliary supply unit NF92 (3 T), a DC/DC converter for generating the internal relay supply voltages.
- test socket connector XX91 (option) (3 T).
- auto-reclosure relay WT91 (option) (4 T).
- pilot wire units ER91 and NR91 (option) (4 T each).

These instructions apply to the basic versions of the distance relays, i.e. relays equipped with EW91 and either KI91, KZ91 or KZ91-1 and also an auxiliary supply unit.

There are two versions of the input transformer and main processing units available, which have resulted from further technical development and which are described in these instructions (see Section 3.12).

 $\left(\begin{array}{c} & \\ & \\ & \\ & \end{array}\right)$

3. DETERMINING THE SETTINGS

All important settings on the distance relays LZ91, LZ92 and LZ92-1 are thumbwheel switches. The corresponding setting formulas are printed on the frontplate (see Figures 9.3 to 9.8).

In each case the setting in the correct dimensions is obtained by inserting the number indicated on the thumbwheel switches in the formula and working it out. The settings themselves (e.g. impedance of the underimpedance starting units) are printed in italics.

Settings which are normally only made once during commissioning are miniature switches located on the PCB's. Information relating to these settings is marked on the frontplate of the KI91, KZ91 respectively KZ91-1 behind the hinged flap of the equipment rack (Figures 9.3 to 9.8). A symbol shows on which PCB the particular functional switch is to be found. The PCB's in the units KI91, KZ91 respectively KZ91-1 are numbered from left to right seen from the front and as marked on the frontplate. (There is no PCB 3 in KI91.) Further information on this can be found in Section 3.9.

3.1 Selecting the operating mode

3.1.1 Distance relay LZ91

A choice can be made between two operating modes using miniature switch 1 on PCB 1 of unit KI91 (Fig. 9.21):

- Mode \checkmark : for solidly grounded systems, i.e. earth faults have to be tripped.
- Mode \triangle : for ungrounded or impedance grounded systems, but only with acyclical phase-preference for cross-country faults.

3.1.2 Distance relay LZ92

As with the distance relay LZ91, a choice can be made between the modes \bigwedge and \bigtriangleup using miniature switch 1 on PCB 1 of unit KZ91. In this case, however, mode \bigtriangleup also permits cyclical (L0 in the code) as well as acyclical (L1 in the code) phase-preference for cross-country faults. This change is made by appropriately positioning soldered link LB7 on PCB 1 (see Fig. 9.22).

Standard phase-preferences are R before T before S (acyclically) respectively R before T, T before S, S before T (cyclically).

3.1.3 Distance relay LZ92-1

In solidly grounded systems in which it is essential that earth faults be tripped mode Δ should be set.

In ungrounded or impedance grounded systems the same phase-preference must be set as is used in the rest of the system. The operating mode is set on the main processing unit KZ91-1 with the aid of switch 1 (Fig. 9.23). This switch has four sections which have to be set in accordance with Table 3.1. The same information is printed on the frontplate behind the withdrawing handle (Fig. 9.8).

	Switch section *			Mode	
No. 1	No. 2	No. 3	No. 4	Phase preference	Setting
OFF	ON	ON	OFF	grounded systems	
OFF	OFF	ON	OFF	RTS acyclically	RTS acyc.
ON	ON	OFF	OFF	RTSR cyclically	RTSR cyc.
OFF	ON	OFF	OFF	TRS acyclically	TRS acyc.
ON	OFF	OFF	OFF	TRST cyclically	TRST cyc.
ON	OFF	ON	OFF	TSR acyclically	TSR acyc.
ON	ON	ON	ON	RST acyclically	RST acyc.
OFF	OFF	OFF	OFF	STR acyclically **	STR acyc.
OFF	OFF	OFF	ON	SRT acyclically **	SRT acyc.

* In position "ON" the white lever is toward the PCB.

** Only for units with the designation "AEND.:B" on PCB 1.

Table 3.1 - Positions of the switch sections of switch 1 on PCB 1 of unit KZ91-1 in relation to operating mode

3.2 Setting the starting units

3.2.1 Overcurrent starting

The pick-up setting of the overcurrent units must be at the very least so high that it cannot be reached by the maximum current in healthy phases. The following must be taken into account when calculating this current:

- In the case of double-circuit lines the load current can briefly double when one line is tripped.
- An additional balancing current ${\rm I}_{\rm A}$ can flow in the healthy phases during earth faults.

A relay which has picked up must also be able to reset at the level of the maximum load current I after a fault has been tripped in the first zone of another relay. Taking the reset ratio and accuracy of the relay into account, the lowest permissible setting is given by:

$$\left(\frac{I}{I_{N}}\right)_{\min} \geq \frac{\left|\overline{I}_{Bmax}^{*} + \overline{I}_{A}^{*}\right|}{I_{N} \times 0.9} + 0.06$$

The maximum permissible setting (I/I) is calculated from the minimum fault current I_K for a fault at the remote end of the next section of line.

Should (I_K/I_N) be less than (I/I_N) according to the above relationship, then a relay with underimpendance starting, e.g. LZ92 or LZ92-1, must be used instead of a distance relay LZ91.

When used in ungrounded or impedance grounded systems (mode \triangle), the current measuring elements of unit KI91 are connected to R, T and E. The sensitivity of the neutral element in the standard versions is twice that of the phase elements (I_{Σ}/I_{ph} = 0.5, I/.../ in the code of the KI91). If some other sensitivity is specified, it can be seen from the relay ordering code:

I/.../sensitivity factor

Should it be impossible to set the phase elements such that they pick up reliably at the lowest phase fault level, but do not pick up at the maximum earth fault current, then a distance relay LZ92 or LZ92-1 must be used.

3.2.2 Underimpedance starting

The following two frontplate (KZ91 or KZ91-1) settings apply to the underimpedance starting units:

- the pick-up of the neutral current element
- the reach of the underimpedance elements.

3.2.2.1 Neutral current earth fault detector

The criterion for the highest setting is:

- The earth fault detector must pick up for all earth faults in grounded systems, respectively all cross-country faults in ungrounded or impedance grounded systems which lie within the set reach of the underimpedance units.

The criterion for the lowest setting is:

- The earth fault detector may not pick up for an earth fault on a single conductor of an ungrounded or impdance grounded system.
- The earth fault detector may not pick up during heavy phase faults due to spurious neutral currents caused by c.t. errors.

A typically recommended value is $I_{\Sigma} = 0.8$ to 1.0 x I for LZ92 and LZ92-1 (standard version of the LZ91: $I_{\Sigma}/I_{M} = 0.5$).

If there is no setting which satisfies both these limits, a neutral voltage polarising ancillary must be added to operate in conjunction with the neutral current element:

- This is available as a separate voltage relay to be inserted into the equipment rack of the distance relay.

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- Either an OR or an AND interlocking logic can be used (see parts of the ordering code headed EK1 or EK2 in the tables of features Table 9.17, Table 9.18 and also Fig. 9.25).

The LED signal labelled "E" will only light up when both the earth fault detector and at least one underimpedance phase element have picked up.

3.2.2.2 Reach of the underimpedance elements

Standard characteristic

The standard underimpedance starting characteristic is a circle with its centre at the origin of the impedance plane. The relationships for an earth fault R-0 are:

 $\overrightarrow{U_{\Delta R}} = \overrightarrow{U_R} - 2 x_A e^{j90^\circ} \overrightarrow{I_R}$ $\overrightarrow{U_{\Sigma R}} = \overrightarrow{U_R} + 2 x_B e^{j90} \overrightarrow{I_R}$

with $X_A = X_B$ X_A replica reactance in forward direction X_B replica reactance in backward direction

Criterion for pick-up: $\overline{U_{\Delta R}} \perp \overline{U_{\Sigma R}}$, $\varphi \geq |90^{\circ}|$ The reach for radial lines and the different kinds of faults is:

- phase-to-phase fault: same as setting

- three-phase fault
$$:\frac{2}{\sqrt{3}}x$$
 setting
- earth fault $:\frac{2}{\sqrt{3}}x$ setting
 $\frac{2}{1+k}x$ setting
 $(k_{OL} = K_{O} \text{ of the line})$

ABB has a computer programme for checking the relay settings for all kinds of faults and fault locations where complex system conditions prevail.

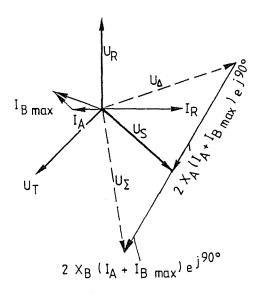
Minimum reach of the underimpedance starting elements

The starters must reliably pick up for a fault at the end of the next section of line (back-up zone). Where the back-up zone is not being taken specifically into account, the setting must be at least 1.3 times the impedance of the protected line. The influence of arc resistance must also be considered in the case of short lines.

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Maximum reach of the underimpedance starting elements

- The considerable increase of load current which can occur on the healthy line when one line of a double circuit is tripped must be taken into account.
- Balancing currents I in the healthy phases during an earth fault must not cause their starters to pick up. The corresponding limit can be determined as follows:



RO earth fault; S starter I_A : balancing current I_{Bmax} : max. load current $2X_A (I_A + I_{Bmax}) e^{j90}$: replica voltage $2X_B (I_A + I_{Bmax}) e^{j90}$: (S phase) criterion for pick-up: $U_A \perp U_{\Sigma}$

$$\overline{U}_{\Delta} = \overline{U}_{S} - 2 X_{A} e^{j90^{\circ}} \overline{I}_{S}$$

$$\overline{U_{\Sigma}} = \overline{U_{S}} + 2 x_{B} e^{j90^{\circ}} \overline{I_{S}}$$

$$\frac{\overline{U}_{\Delta}}{\overline{U}_{\Sigma}} = \frac{\frac{U_{S}}{2\overline{I}_{S}} - x_{A}e^{j90^{\circ}}}{\frac{\overline{U}_{S}}{2\overline{I}_{S}} + x_{B}e^{j90^{\circ}}} = \frac{\overline{A} - x_{A}e^{j90^{\circ}}}{\overline{A} + x_{B}e^{j90^{\circ}}} \quad \text{with } \overline{A} = \frac{\overline{U}_{S}}{2\overline{I}_{S}}$$

Fig. 3.2 - Example for $X_A \neq X_B$

The limits can be expressed mathematically as follows: - in grounded systems

$$Z = \frac{U}{2 | (I_{Bmax} + I_{A}) |}$$
 (Ohm/Phase)

- in ungrounded or impedance grounded systems

$$Z = \frac{U_v}{2 I_{\text{Bmax}} \times 1,25}$$
 (Ohm/Phase)

where

U lowest phase-to-neutral voltage of the healthy phases for an earth fault (U = 0.85 x min. rated voltage)

U, lowest phase-to-phase rated voltage

1.25 safety factor

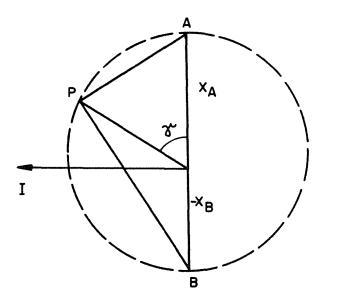
3.2.2.3 Special underimpedance starting characteristics

If it is impossible to satisfactorily set the reach according to Section 3.2.2.2, then the following procedure should be tried.

The influence of the balancing currents must be carefully analysed. For examining the S phase equations for an R-O earth fault see Section 3.2.2.2.

The relationship $\rm X_B/\rm X_A$ may be changed and the forward reach and the backward reach become different. The origin of the circular characteristic is shifted in the X-direction.

ं क



The diameter of circular loci of the pick-up point P is AB.

Fig. 3.3 - Circular underimpedance starting characteristic

The angle γ is depending on the network conditions. With the aid of computer calculations γ was found to be \geq 30°.

It is possible to change the angle \checkmark from 90° to 108°, in which case the characteristic is no longer a circle (see Fig. 3.4).

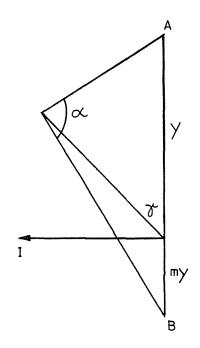


Fig. 3.4 - Special characteristic

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	Settings			
گد[0]	Q[°]	m	У	
090 30 30 (90) 30	90 90 108 (108) 108	1 0,57 1 (1) 0,57	1 1,04 1,18 (1,38) 1,22	

Table 3.5 - Permissible settings in relation to a circular characteristic

Such characteristics have a considerably extended reactive reach compared with a circle concentric to the origin. The equation for a solidly grounded system then becomes:

$$X_{A} = \frac{U \times Y}{2 (|I_{A}| + |I_{L}|) n} \qquad (X_{A} > X_{B})$$

where

$$n = \frac{1 + k_{o}}{2}$$
 for starting (Z///.../ in the order number)

It is permissible to use the algebraic sum instead of the geometric sum $(\vec{I}_A + \vec{I}_L)$, since it represents the worst case.

The effective reach along the line is dependent on the characteristic angle of the line and the kind of fault (see Tables 3.6 to 3.8):

Line angle	Characteristic			
φ __ [°]	Circle $X_B = X_A$	Circle $X_B = 0.57 X_A$	Lense $X_B = X_A$	Lense $X_B = 0.57 X_A$
90	1	1	1	1
80	1	1	0.94	0.94
70	1	0.99	0.89	0.88
60	1	0.97	0.85	0.82
50	1	0.94	0.81	0.76
40	1	0.91	0.78	0.70
30	1	0.87	0.76	0.66
20	1	0.83	0.74	0.61
10	1	0.79	0.73	0.59
0	1	0.76	0.73	0.55

Lens: **A** = 108°

Table 3.6 - Relative reach in relation to characteristic and line angle for a phase-to-phase fault

Line angle		Characteris	stic	
$\varphi_{\rm L}$ [°]	Circle	Circle	Lense	Lense
	X _B = X _A	$X_B = 0,57 X_A$	$X_B = X_A$	$X_B = 0,57 X_A$
90 80 70 60 50 40 30	1.15 1.15 1.15 1.15 1.15 1.15 1.15 1.15	1.11 1.08 1.04 1 0.94 0.92 0.87	0.98 0.94 0.89 0.88 0.85 0.84 0.84	0.95 0.88 0.82 0.76 0.72 0.67 0.64
20	1.15	0.83	0.83	0.61
10	1.15	0.79	0.85	0.59
0	1.15	0.75	0.88	0.58

Table 3.7 - Relative reach in relation to characteristic and line angle for a three-phase fault (referred to the reach of a circle about the origin for a phase-to-phase fault)

Line angle		Characteri	stic	
j	Circle	Circle	Lense	Lense
φ [°] L	$X_{B} = X_{A}$	$x_{B} = 0,57 x_{A}$	$x_{B} = x_{A}$	$x_{B} = 0,57 x_{A}$
90	1.15	1.11	0.98	0.95
80	1.15	1.08	1.0	0.94
70	1.15	1.06	1.0	0.93
60	1.15	1.04	1.03	0.93
50	1.15	1.02	1.05	0.95
40	1.15	1.01	1.08	0.97
30	1.15	1.0	1.11	0.98
20	1.15	1.0	1.12	1.0
1.0	1.15	1.0	1.16	1.02
0	1.15	1.0	1.21	1.05

Table 3.8 - Relative reach in relation to characteristic and line angle for a three-phase fault (referred to the <u>reach of the same characteristic</u> for a

(referred to the reach of the same characteristic for a phase-to-phase fault)

Reverse reach

The effective forwards (X_A) and reverse (X_B) reaches can be entered in the diagram of Fig. 3.2. The ratio X_A/X_A is set on PCB 3 by inserting the appropriate value for resistor Wi4 from 0.26 to 3.91 (Z./0.26/ to /Z.3.91/ in the ordering code, see Fig. 9.26).

Extreme forwards reaches

Values of X_B/X_A greater than 1 are of consequence when an extreme forwards reach is needed. This is achieved by reversing the primary current connections to the distance relay and then reversing the measuring direction of the distance unit (using switch 8b on PCB 8, order number HESG 439 686, respectively switch I \rightleftharpoons on the front of PCB 8, HESG 440 718). The forwards reach then corresponds to the frontplate setting multiplied by the set ratio X_B/X_A .

Lenticular characteristic (Z2/.. in the ordering code)

The pick-up criterion in the diagram of Fig. 3.2 is no longer determined by $\overline{U}_{\Delta} \perp \overline{U}_{\Sigma}$, but instead by $\not\prec (\overline{U}_{\Delta}, \overline{U}_{\Sigma}) = 108^{\circ}$. The change is made with the aid of the soldered link LB3 on PCB 3 (see Fig. 9.26).

Earth fault reach (Z.///.../ in the ordering code)

The reach for earth faults corresponds to the frontplate setting providing the k factor for the starting units matches that of the line (k = k). The starter k is set using resistor Wi5 on PCB 2 in KZ91 or KZ91-1 (see Fig. 9.25).

The above changes may be used singly or in conjunction with each other!

3.3 k_____factor for the distance measuring system

The compensation of the zero-sequence impedance is calculated from the positivesequence impedance Z_{T} and zero-sequence impedance Z_{OL} of the line or cable.

$$|\mathbf{k}_{0}| = \frac{1}{2} \cdot \frac{\varphi_{\mathrm{K}}}{2} = \frac{1}{3} \cdot \frac{\overline{z}_{0\mathrm{L}}}{\overline{z}_{\mathrm{L}}} = \frac{1}{2}$$

This zero-sequence compensation factor k_0 is set on the front of the EW91 (according to HESG 438 454) as follows:

EW91 code K.O: Only the magnitude of (|k|) is set. This is generally all that is necessary in the case of overhead lines (thumbwheel switch φ_k set to zero).

EW91 code K.1: Both the magnitude (|k|) and phase-angle (${}^{\varphi}k$) are set, which is of consequence above all in cable systems.

It is always possible to set the magnitude and phase-angle in the case of the input transformer unit according to HESG 440 754 (see Section 3.12).

The k_0 factor only bears an influence on the distance measuring system.

3.4 Impedance settings of the various zones

3.4.1 Determining the reaches of the distance zones

In order to calculate the settings the fault impedances and the phase-angles of the sections of line to be protected must be known.

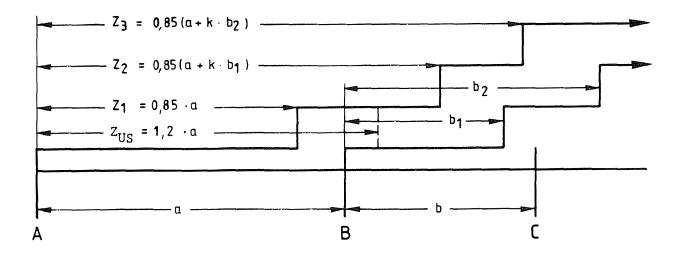


Fig. 3.9 - Reaches of the various distance zones

$Z_{1}, Z_{2}, Z_{3}, Z_{4}$	zone impedances
	infeed factor which takes into account an intermediate
_	infeed and the consequential apparent increase in the
	impedance by the relay
a, b	corresponding line impedances
Z _{US}	overreach zone impedance $Z_{US} = 1.5 Z_1$

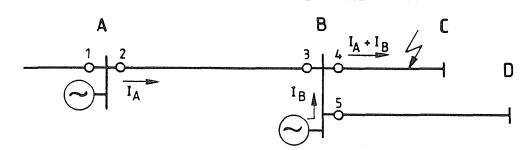


Fig. 3.10 - Exampel for the calcuation of k; check the overreach for k > 1 in the event that the source for B is out of operation: $I_A + I_B$

$$k = \frac{A}{I_A} \ge 1$$

where

I,	max.	possible	fault	current
I I 1 ^B to 5	min.	possible	fault	current
1 ^D to 5	dista	ance relay	ys	

3.4.2 Determining the settings m, and N,

Secondary line impedances

$$\mathbf{Z}_{\text{LS}} = \frac{\mathbf{Z}_{\text{LP}}}{\frac{\mathbf{r}_{\text{U}}}{\mathbf{r}_{\text{I}}}} = \frac{\mathbf{Z}_{\text{LP}}}{\mathbf{r}_{\text{Z}}}$$

where

The secondary resistance and reactance values are calculated in the same manner.

Calculating X

Before the values m, and N, can be set on the main processing unit it is necessary to calculate the reactances X. These are not exactly equal to the line reactances X (line impedance $Z_{L} = R_{L} + jX_{L}$), because of the inclination of the relay reactance characteristic by the angle \ll .

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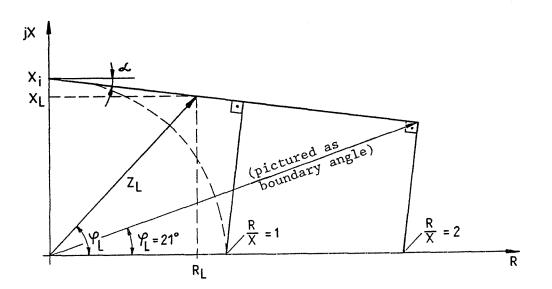


Fig. 3.11 - Line impedance Z_{L} entered in the relay characteristic

Thus to obtain the reactances X_i , the line reactances have to be corrected by the factor k_x :

 $X_i = X_{Li} \cdot k_X$

The correction factor k_x is given by:

 $k_{X} = 1 + \frac{\tan \alpha}{\tan \varphi}$

If the phase-angle \mathcal{H} is less than 40°, as can be the case in cable systems, there are two possibilities for grading as follows:

- grading in relation to ${\rm X}_{\rm L}$ - grading in relation to ${\rm R}_{\rm L}$

Grading with X_L :

If the setting R/X is greater than 1, e.g. R/X = 2 (see Fig. 3.11), then the factor k_X according to the table 3.12 is used.

Grading with R_L :

If the setting of the polygon is made in relation to the line resistance RL with the arch resistance compensation R/X = 1 (see Section 3.5), then R_{Li} is used instead of X_{Li} and k_R instead of k_X for grading (see Table 3.12).

Where for $\boldsymbol{\Psi}_{L} < 40^{\circ}$: $X_{i} = k_{R} \cdot R_{Li}$; with $k_{R} = 1 \tan \boldsymbol{\alpha} \cdot \tan \boldsymbol{\varphi}$

Line angle	R/X =	= 1	R/X = 2
φL (°)	k _X	k _R	k _X
0	-	1	-
5	-	0,992	-
10 15	-	0,985 0,977	_
20	-	0,968	_
21	-	0,966	1,228
25	-	0,959	1,188
30	_	0,95	1,152
35		0,939	1,125
40	1,104	0,927	1,104
45	1,087	-	1,087
50	1,073	-	1,073
55	1,061	-	1,061
60	1,05	-	1,05
65	1,041		1,041
70	1,032		1,032
75	1,023		1,023
80	1,015	-	1,015
85	1,008	-	1,008
90	1	-	1

Table 3.12 - Factors k_{χ} and k_{R} in relation to φ (α = 5°)

Example:

System voltage	: U	= 60 kV
P.t. ratio	: К _U	= (60 kV / $\sqrt{3}$) / (110 V / $\sqrt{3}$) = 545.45
C.t. ratio	: K _I	= 200 A / 5 A = 40
Primary zone impedances	: Z	= 4 Ohm, Z_{Lp2} = 6 Ohm, Z_{Lp3} = 8 Ohm
Phase-angle of line	:	= 60°
Relay ratings	: I _N	= 5 A, $U_{\rm N}$ = 110 V
Reactance line slope	: α	= 5°

Firstly the secondary reactances must be calculated from the primary values:

$$X_{L} = X_{Lp} \cdot x \frac{\ddot{u}_{I}}{\ddot{u}_{II}}$$
 and $X_{Lp} = Z_{Lp} \times \sin \varphi$

The line reactances for the three zones become:

$$X_{L1} = 0.254$$
 Ohm, $X_{L2} = 0.381$ Ohm, $X_{L3} = 0.508$ Ohm
The correction factor k_X is:

$$k_{X} = 1 + \frac{\tan 5^{\circ}}{\tan 60^{\circ}} = 1.05$$

The zone reactances X; can now be calculated:

$$X_{i} = X_{Li} \cdot k_{X}$$

 $X_{1} = 0.267 \text{ Ohm}, X_{2} = 0.4 \text{ Ohm}, X_{3} = 0.533 \text{ Ohm}$

Calculating the setting factors m. and N.

The factors m_i and N_i can be calculated from the following formula by inserting the reactances X_i and the relay ratings:

$$X_{i} = \frac{H}{I_{N}} \cdot m_{i} \cdot \frac{100}{N_{i}}$$
(1)

where

Н	factor depending on the rated voltage, H = 1 for U = 100 to 130 V H = 2 for U = 200 to 260 V
	(see ordering code U on the rating label)
I _N	relay rated current 1 A, 2 A, 5 A (see ordering code A on the rating label)
m. i	impedance measuring range (see Table 3.13)
N _i	"percentage" impedance setting for the distance zones
i	index denoting the distance zone, $i = 1$ for zone 1 etc.

The quotient H/I takes the relay ratings into account, so that the same setting formula can be used for all the relays.

The setting range m can be set individually for each of the zones on the frontplate of the KI91, KZ91 respectively KZ91-1 (see Table 3.13 and Figures 9.5 to 9.8).

Switch m. setting	I	II	III
^m 1	0.1	0.5	5
m ₂ or m ₃	0.1	1	1.0

Table 3.13 - Values of the factor m for zones 1 to 3 represented by the settings of switch m.

The percentage N can also be set from 0 to 99 in steps of 1 individually for each zone. The reciprocal setting has the advantage of a fine setting for short zones.

The m setting should permit N to be set as accurately as possible, i.e. the value of N should be as high as possible. An N of 100 is therefore inserted initially in order to determine the best setting range m.

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From (1) with N = 100:

$$m_{i} = \frac{X_{i}}{H/I_{N}}$$

The value of m obtained in this way is then rounded to the next smaller value as given in Table 3.13.

By inserting this value of m in equation (1) the actual N setting can now be obtained.

$$N_{i} = \frac{H}{I_{N}} \cdot \frac{m_{i}}{X_{i}} \cdot 100$$

Example:

$$N2 = 0.2 \cdot \frac{1}{0.4} \cdot 100 = 50$$
$$N3 = 0.2 \cdot \frac{1}{0.54} \cdot 100 = 37$$

Main processor unit settings:

thumbwheel switch
$$m_1 = II$$

 $m_2 = II$
 $m_3 = II$
thumbwheel switch $N_1 = 38$
 $N_2 = 50$
 $N_3 = 37$

3.5 Arc resistance compensation

The ratio R/X can be set between 1 and 5 in steps of 1 on a control on the front of the main processing unit. The actual setting depends on the value of arc or earth resistance to be expected in relation to the reactance of the line.

Typical settings for overhead lines: R/X = 1, 2 or 3.

A setting of R/X = 1 is permissible in cable systems, since high arc resistance are not to be expected. Where the phase-angle of the positive-sequence impedance is less than 40° the ratio R/X is set to 1 and the R_L is used for grading the relays.

According to van C. Warrington arc resistance can be calculated from:

$$R_{B} = \frac{28700 \text{ d}}{1.4}$$

where

d length of the arc in m I current in A R_B arc resistance in ohms

In the impedance plane it can be seen that the influence of arc resistance varies with the kind of fault, because the unit is ohms per phase. A given fault resistance R_{p} in the R/X plane is as follows:

- earth fault : $R = R_F / (1 + |k_o|)$ (for overhead lines) - phase-to-phase fault : $R = R_F / 2$

In grounded systems R_{p} will be greatest for an earth fault. For a given line impedance $Z_{L} = R_{L} + jX_{L}$ the required arc resistance compensation thus becomes:

$$R/X = \frac{R_{L} + R}{X_{L}}$$

3.6 Direction of measurement

The direction of measurement is determined by the position of switch 8b on PCB 8 of the main processing unit (up to order number HESG 439 686, see Section 3.12). From order number HESG 440 718 (PCB 8) provision is made for selecting the direction of measurement on the frontplate of the main processing unit with the control marked I \rightarrow (see Figures 9.6 and 9.8). In either case there is no influence on the starting units.

With the switch in position $I \rightarrow$ and the relay connected according to the wiring diagram, i.e. with the neutral of the c.t's at the ends of the windings nearest the busbars, the relay measures into the line.

Where the relays LZ92 or LZ92-1 are being used with an off-set circular or lenticular starting characteristic, the reach of the starters must be checked and where necessary the c.t. connections reversed (see Section 5.3).

Depending on the main processing unit code (KI91, KZ91 or KZ91-1) in the ordering code, one zone can be arranged to measure in the reverse direction. This is done with switch respectively plug-in link 8a on PCB 8 (see Figures 9.30 and 9.31).

From the above it follows that it is necessary to state which zone is to be reversed when ordering. However, should it be found during commissioning that a different zone than the one ordered should be reversed, then the change can be made with the soldered link LB9 on PCB 6 of the main processing unit KI91, KZ91 or KZ91-1 (see Fig. 9.28).

The PCB switch 8a (plug-in link 8a from order number HESG 440 718) on PCB 8 then determines whether all zones measure in the forwards direction (position 1), or whether the zone according to the ordering code (1, 2, 3 or 4) should measure in the reverse direction (position 2). This is not fitted in the case of code R0. and order number HESG 439 686 (PCB 8).

The directional sensitivity of the LZ91 and LZ92 is 100 mV (U = 100 to 130 V). The directional sensitivity of the cross-polarized LZ92-1 is unlimited for all earth and phase-to-phase faults; a memory feature provides the polarizing voltage to facilitate measurement of three-phase faults.

3.7 Zone 4

Normally the operating characteristic of zone 4 is that of the starting unit, which is permitted to trip on its own after the time t (code for KI91 and KZ91: E00, links LB11 and LB12 on PCB 7 soldered in).

Provision is made for using the X or R limits of zone 3 as blinders for zone 4 (code $E \neq 0$), which is achieved by removing the corresponding soldered link on PCB 7 (X blinder with LB11 removed and R blinder with LB12 removed).

Switch 7 on PCB 7 (Fig. 9.29) enables zone 4 to operate either directionally (position 1) or non-directionally (position 2). Non-directional operation means that the whole of the starting characteristic is used for zone 4, compared with directional operation with the starting characteristic cut off by the directional measurement so that tripping can only take place in the forwards direction.

3.8 Time step characteristic

The operating times of zone 2 and higher include the basic operating time of the relay without any intentional delay plus the corresponding set time delay.

No provision is made for delaying zone 1 in the main processing units PCB 8 acc. to HESG 439 686 (Section 3.12). If it is desired to delay zone 1 in this case, then the normal zone 1 can be blocked with PCB switch 6a so that zone 2 becomes zone 1 with an operating time t2 etc., the relay having a total of three zones.

Zone 1 can be delayed separately in the case of main processing units with PCB 8 acc. to HESG 440 718 (Section 3.12) between 0 and 15 x T_N (T_P = period of rated system frequency). The desired time is set using soldered link LB18 on the soldered side of PCB 8 (see Fig. 9.32).

The delays of the zones 2 and 4 are set digitally on thumbwheel switches in the following ranges:

2nd.	time	step:	0	to	0.99	s	in	steps	of	0.01	s		
3rd.	time	step:	0	to	0.99	s	in	steps	of	0.01	s	and	
			0	to	9.9	s	in	steps	of	0.1	s		
4 th	time	step:	0	to	9.9	s	in	steps	of	0.1	s		

The alternative setting ranges for the 2nd. time step are determined by selecting the factor k_{\pm} with PCB switch 6b (see Fig. 9.28).

The minimum grading time between different LZ91 respectively LZ92 relays should not be less than the sum of the circuit-breaker operating time plus 150 ms. The 150 ms are the sum of relay operating time + relay reset time + a safety margin.

3.9 Symbols and significance of the switches on the main processing unit

The legend below the white line on the frontplate of the KI91, KZ91 or KZ91-1 (see Figures 9.5 to 9.8), which is behind the hinged lower flap when the unit is in the rack, shows the symbols, the number of the PCB's and the locations of the PCB switches. The numbers 1 to 8 (from left to right) refer to the PCB's, below which can be found the symbols corresponding to the switch positions and an indication of the switch location on the PCB.

For example, the designation 6a refers to the first switch from the frontplate towards the rear on PCB 6. Just so 6b is the second switch from the front on PCB 6. The locations of the switches can also be seen from the PCB drawings in the appendicies.

The symbols and functions of the switches are as follows:

t

switch 1	position 1 \land position 2 \land	for grounded systems for non-grounded systems
switch 6a	position 1 t_1 position 2 t_1	zone 1 blocked zone 1 in operation

switch 6b	position 1 position 2		multiplier for the 3rd. time step multiplier for the 3rd. time step
switch 7	position 1	t —►	zone 4 directional
	position 2	t₄ ←►	zone 4 non-directional

PCB 8 according to HESG 439 686 (see Section 3.12)

switch 8a	position 1 position 2	8b t ♣⊃	direction has indicated on 8b direction as indicated on 8b, but <u>one</u> zone reversed (see Section 3.6). Switch 8a is not fitted in all versions.
switch 8b	position 1 position 2		reverse direction forwards direction, if relay connected as in our diagram.

PCB 8 according to HESG 440 718 (see Section 3.12)

plug-in link 8a	position 1	I	direction as set on I 📥 on the front- plate
	position 2	t ᠫ	direction as set on I $\overrightarrow{}$ on the front- plate, but <u>one</u> zone reversed.
plug-in link 8b	position 1		overreaching (intertripping scheme, see Section 3.10)
	position 2	PUTT	permissive underreaching transfer tripping (where fitted).

3.10 Main processing unit equipped for intertripping schemes

Main processing units with PCB 8 acc. to HESG 440 718 (see Section 3.12) are equipped as standard with the auxiliary components for operating in intertripping schemes. On these units either an acceleration scheme (zone extension) or permissive underreaching transfer tripping can be selected using plug-in link 8b on PCB 8 (see Fig. 9.31). In the case of an acceleration scheme a signal from the opposite station causes the receiving relay to switch to overreaching, whilst in the permissive underreaching scheme the relays have a normal underreaching setting and the receiving relay trips, providing a starting unit has picked up. In special cases the PUTT scheme can also include the directional measurement of the receiving relay (soldered link LB20, see Fig. 9.32).

3.11 Frontplate signals

Depending on the ordering code of main processing units with PCB 8 acc. to HESG 439 686, the frontplate signals are either in operation (code FA1) or are suppressed (code FA2).

In the case of main processing units with PCB 8 acc. to HESG 440 718 (see Section 3.12) and code FA1 a further choice can be made using soldered link LB19 on PCB 8 (see Fig. 9.32) between:

- LED indication of all faults maintained until reset (LB19 open)
- LED indication of all faults, but only maintained if the relay itself trips (LB19 closed).

3.12 Versions of the input transformer and main processing units

There are several versions of the units EW91, KI91, KZ91 and KZ91-1, which can be recognised in the first place from the different arrangements of their frontplates (compare Figure 9.3 with 9.4 and Figures 9.1 and 9.2 with Figures 9.5 to 9.8). The differences concern essentially PCB 1 of the input transformer unit EW91 and PCB 8 of the main processing unit. The units and PCB's can, of course, be distinguished by their part numbers on their rating plates.

The part numbers of the earlier versions are:

input trasformer unit EW91	HESG 438 454 Rl to Rl6
PCB 1 of EW91	HESG 438 048 (see Fig. 9.19)
input transformer unit EW91	HESG 440 754 R1 to R8
PCB 1 of EW91	HESG 440 752 (see Fig. 9.20)
main processing unit KI91	HESG 440 079 R1 to R4
main processing unit KZ91	HESG 440 080 R1 to R4
main processing unit KZ91-1	HESG 440 342 R1, R2
PCB 8 in the main processing unit	HESG 439 686 (see Fig 9.30)

The part numbers of today's versions are:

input transformer unit EW91a PCB 1 of EW91a

main processing unit KI91HESG 440 079 R11 to R14main processing unit KZ91HESG 440 080 R11 to R14, R22main processing unit KZ91-1HESG 440 342 R11 to R14, R22PCB 8 in the main processing unitHESG 440 718 (see Fig 9.31)

Since these instructions refer to all versions, the part number is always stated in order to distinguish the units. The PCB-drawings necessary for the settings may be found in the appendices.

HESG 441 721 R1 to R8

HESG 441 585 (see Fig. 9.20)

4. CHECKING THE SHIPMENT

Unpacking, visual inspection

Should the shipment be found to be damaged upon receipt, a claim must be lodged immediately in writing with the last carrier and the facts notified to ABB Relays AG, Department ESA-R, CH-5401 Baden.

Checking the equipment ratings

- Check that the relay rated current agrees with the c.t. secondary rated current.
- Check that the relay rated voltage agrees with the p.t. secondary rated voltage.
- Check that the rated frequency of every unit is the same as the local system frequency.
- Check that the ordering code of each unit (given on the rating plate) agrees with the one given in the order (see Table of Versions and Ordering Codes in the appendices).

5. INSTALLATION AND WIRING

5.1 Relay location and ambient conditions

Since every piece of technical equipment can be damaged or destroyed by inadmissible ambient conditions,

- the relay location should not be exposed to excessive air pollution (dust, aggressive substances)
- severe vibration, extreme changes of temperature, high levels of humidity, surge voltages of high amplitude and short rise time and powerful magnetic fields should be avoided as far as possible.

In any event it is essential for the limits and ranges given in the section "General Data" of the corresponding Data Sheet to be observed.

5.2 Checking the wiring

- Check that the relay has been wired in strict accordance with the corresponding wiring diagram (see appendicies).
- Check that the phase references of c.t. and p.t. connections correspond.
- Check that the ratings of c.t's, p.t's and auxiliary supply agree with the those of the relay (EW91, DC/DC converter and optp-coupler unit).
- Check that the same phase is used as R phase in all the stations.
- Check that the phase-sequence is correctly R-S-T.

5.3 C.t. connections

Generally the c.t's are wound in the sense and have the terminal designations shown in Fig. 5.1. With the normal arrangement of K connected towards the busbar and L towards the line, the secondary terminals k and l are connected in strict accordance with the wiring diagram. Should the primary of a winding be connected in the reverse sense, i.e. L towards the busbar and K towards the line, the secondary connections to the terminals k and l must also be reversed.

Should there be any doubt as to the sense of any of the windings, the following method can be used to determine the polarity of the secondaries. Terminals K and L are connected to a d.c. source of about 4 V and k and 1 to a polarized voltmeter as shown in Fig. 5.1. The polarity is correct, if there is a positive deflection when the switch S is closed.

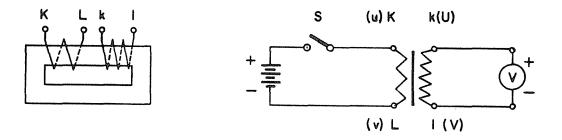


Fig. 5.1 - Instrument transformer terminals and polarity check (U, u, V and v apply to p.t's)

If the star-point of the c.t. windings is not on the busbar end of the windings as shown in the wiring diagram, provision is made on the relay for reversing the direction of measurement with the switch $I \stackrel{\longrightarrow}{\longleftarrow}$ (switch 8b respectively frontplate switch). The position of this switch has no influence on the starting units (see Section 3.6).

It is particularly important for the connections and c.t. circuit shorting links of the input transformer unit EW91 with part number HESG 438 454 (see Section 3.12) to be carefully checked. The terminals must be connected in accordance with the rated current as follows:

IN	=	1	А	Al(R),	A4(S),	А7 (Т)
I I N	=	5	A	A2(R),	A5(S),	A8 (T)
con				A3(R),	A6(S),	A9(T)

Input transformer units with the part number HESG 440 754 resp. HESG 441 721 only have terminals for the rated current as ordered.

5.4 P.t. connections

Should there be doubt as to the sense of the p.t. windings, then their polarity can be ascertained in the same manner as for c.t's. In this case, however, the battery is connected to the secondaries (see terminal references in brackets in Fig. 5.1). Caution! First switch the voltmeter to a high voltage range. All the phases of three-phase p.t's must be tested. For each relay the p.t's must be fused at a rating of 10 A.

The secondary circuits of both c.t's and p.t's should be grounded at only one point.

5.5 Auxiliary supply

Check that the polarity of the auxiliary d.c. is correct. The auxiliary d.c. supply must remain within the permissible range of variation of the DC/DC converter supplied with the relay under all operating conditions (see technical data of the DC/DC converter).

5.6 Loading of tripping and signalling contacts

Check the section "Contact Data" of the corresponding Data Sheet to ensure that the ratings of the contacts are adequate for the duty.

5.7 Opto-coupler inputs

Check the voltage against the order code and the polarity of all opto-coupler inputs (opto-coupler unit: M115, inputs Al2 and Bl2 of the input transformer unit EW91 and any other opto-couplers in interfacing units). If the opto-coupler input of the EW91 is used to block the relay by an auxiliary N/O contact of an m.c.b. in the p.t. circuit (auxiliary contact closed when the main contacts are closed), the link W61 must be changed to output Q (see Figures 9.19 respectively 9.20).

6. COMMISSIONING

6.1 Checking the settings prior to commissioning

Before commissioning the relay, that is before the protected unit is energised, it is necessary to:

- perform all the checks according to Sections 4 to 5.7

- set the relay as described in Section 3.

6.2 Inserting the relay and switching on the auxiliary supply

It is only permissible to insert or withdraw the plug-in units with the auxiliary supply switched off!

Care must be taken that each plug-in unit is inserted in its designated position in the equipment rack. Insert the respective unit carefully with the withdrawing handle horizontal until resistance is felt. Now raise the withdrawing handle, which acts as a lever to complete the insertion of the relay, to the vertical position and push it fully home ensuring that it latches securely. A unit is correctly inserted, if the hinged flaps at the front top and bottom edges of the rack can be lowered, respectively raised, without difficulty.

It is only permissible to switch on the auxiliary supply after all the units have been inserted.

6.3 Test points on the unit EW91

The most important signals are available for measurement at ten respectively twelve sockets (see Section 3.12) on the front of the input transformer unit EW91. These signal outputs are short-circuit-proof, but on no account should they be used for injecting singals. The inner diameter of the sockets is 2 mm and a normal multi-meter may be used for measurement, providing its internal resistance on the range being used is greater than 1 MOhm.

The following signals and quantities are available at the sockets:

- 0 Common zero for measuring the voltages at all the other sockets (clean supply 0 V).
- B1 Blocking signal. The normal voltage at this socket is 10 V, but when the relay is blocked by either the auxiliary supply monitoring circuit or an external blocking signal the voltage falls to $\leq +2$ V.
- -o/o- Tripping signal. This socket is at +10 V whilst the relay is emitting a tripping signal to the circuit-breaker. The signal is tapped off just before the auxiliary tripping relay and thus appears 2 to 5 ms before the tripping contact actually closes.

- I_R, I_S, I_T Voltages proportional to the phase currents taken from across the replica impedances are available at these three sockets (see Section 6.4 for details).
- U_R, U_S, U_T Phase voltages. The voltages are taken from behind the input transformers (see Section 6.4 for details).
- k_0 , I Zero-sequence compensation. A voltage U can be measured at this socket which is proportional to the product of the neutral current I_{Σ} and the compensating factor $|k_0|$.

The relationship is: $U_{M}(V) = 0.125 \times \frac{|k_{O}| \times I_{\Sigma}(A)}{I_{M}(A)}$

Input transformer units with the part number HESG 440 754 resp. HESG 441 721 have these two sockets in addition. Caution! The circuit breaker can be tripped by connecting these sockets and pressing one of the starter test buttons. This procedure energizes the auxiliary tripping relay in the EW91 without the distance relay having to measure, providing terminal E11 of the EW91 is connected to terminal E63 of the main processing unit (with PCB 8 according to HESG 440 718).

6.4 Checking load voltages and currents

Load voltages and currents can be measured at the sockets on the front of the unit EW91 as follows:

- First switch on the auxiliary d.c. supply.
- The phase-to-neutral voltages are reduced by a factor of 15 for rated voltages between 100 and 130 V (code U1) and a factor of 30 for rated voltages between 200 and 260 V (code U2).
- The load currents are converted into proportional voltages according to the relationship 0.125 x I/I $_{_{\rm NI}}$ (V).
- The neutral current should be approximately zero under load conditions.
- The phase sequence of the voltages and currents can also be checked using an oscilloscope.
 The voltage proportional to the current of a particular phase lags the phase voltage by approx. 90° if an ohmic load current flows towards the protected line and the relay is conected according to Fig. 9.9.
- Note: The replica voltages can contain more harmonics than the primary currents due to the physical properties of the replica impedances, but this has no influence on the operation of the relay.

6.5 Testing the distance relay using the test buttons

- In order to test the relay using the test buttons, load voltages must be applied and a load current in excess of 0.2 x I $_{\rm N}$ must be flowing.
- Pressing a starter test button simulates a fault on the corresponding phase or phases.

Mode	Test button	Simulated fault 1)	Starter	Signals	Voltage used for meas.
\downarrow	R	RO	R	R	RO
corres-	S	SO	S	Е	SO
ponds to	Т	TO	т	Т	TO
position 1	RS	RS	RS	RS	RS
switch 1	ST	ST	ST	ST	ST
(KI91)	TR	TR	TR	TR	TR
	RST	RST	RST	RST	RST
\bigtriangleup	R	RS	R	R	RS
corres-	Е	rso, sto	E	Е	SE
ponds to	т	ST	т	т	TS
position 2	RE	RSO, RsO, RtO	RE	RE	RE
switch 1	TE	STO, STO, TrO	TE	TE	TE
(KI91)	TR	TR, RST	TR	TR	TR
	RTE	RSTE, RTE	RTE	RTE	RE

1) Small letters: more distant cross-country fault

Table 6.1 - Faults simulated by the test buttons of the distance relay LZ91

Mode	Test button	Simulated fault 2)	Starter	Signals	Voltage used for meas.
	R	RT	R	R	RT
\sim	S	SR	S	Е	SR
corres-	т	TS	т	Т	TS
ponds to	Е	and one		Eext	
position 1	RE	RE	RE	RE	RE
switch 1	SE	SE	SE	SE	SE
(KZ91)	TE	TE	TE	TE	TE
	RS	RS	RS	RS	SR
	ST	ST	ST	ST	TS
	TR	TR	TR	TR	RT
	RSE	RSE	RSE	RSE	SR
	STE	STE	STE	STE	TS
	TRE	TRE	TRE	TRE	RT
	RST	RST	RST	RST	RT
	RSTE	RSTE	RSTE	RSTE	RT
\bigtriangleup	R	RT	R	R	RT
	S	SR	S	S	SR
corres-	T	TS	т	Т	TS
ponds to	E		E	Eext	
position 2	RE	RsO, tRO	RE	RE	RE
switch 1	SE	sto, rso	SE	SE	SE
(KZ91)	TE	sTO, TrO	TE	TE	TE
	RS	RS	RS	RS	SR
	ST	ST	ST	ST	TS
	TR	TR	TR	TR	RT
	RSE	RSE	RSE	RSE	RE(SE) 1)
	STE	STE	STE	STE	TE
	TRE	TRE	TRE	TRE	RE
	RST	RST	RST	RST	RT
	RSTE	RSTE	RSTE	RSTE	RT

Letters in brackets: cyclical phase-preference
 Small letters: more distant cross-country fault

Table 6.2 - Faults simulated by the test buttons of the distance relay LZ92

÷E

Mode	Test button	Simulated fault 1)	Starter	Signals	Distance measured using	Direction determined using 2)
	R	RT	R	R	RT	ST
\downarrow	S	SR	S	S	SR	TR
	т	TS	т	т	TS	RS
switch 1	E			Eext		
(KZ91-1)	RE	RE	RE	RE	RE	ST
	SE	SE	SE	SE	SE	TR
1 OFF	TE	TE	TE	TE	TE	RS
2 ON	RS	RS	RS	RS	SE	TR
3 ON	ST	ST	ST	ST	TS	RS
4 OFF	TR	TR	TR	TR	RT	ST
	RSE	RSE	RSE	RSE	SR	TR
	STE	STE	STE	STE	TS	RS
	TRE	TRE	TRE	TRE	RT	ST
	RST	RST	RST	RST	RT	ST
	RSTE	RSTE	RSTE	RSTE	RT	ST
R before	R	RT	R	R	RT	ST
T before	S	SR	S	S	SR	TR
S	T	TS	т	т	TS	RS
acyclical	E		E	Eext		
	RE	RsO, tRO	RE	RE	RE	ST
switch 1	SE	sto, rso	SE	SE	SE	TR
(KZ91-1)	TE	sTO, TrO	TE	TE	TE	RS
	RS	RS	RS	RS	SR	TR
1 OFF	ST	ST	ST	ST	TS	RS
2 OFF	TR	TR	TR	TR	RT	ST
3 ON	RSE	RSE	RSE	RSE	RE	ST
4 OFF	STE	STE	STE	STE	TE	RS
	TRE	TRE	TRE	TRE	RE	ST
	RST	RST	RST	RST	RT	ST
	RSTE	RSTE	RSTE	RSTE	RT	ST

Small letters: more distant cross-country fault
 Directional measurement: same current as distance meas.
 Table 6.3 - Faults simulated by the test buttons of the distance relay LZ92-1

Mode	Test button	Simulated fault 1)	Starter	Signals	Distance measured using	Direction determined using 2)
T before T	R	RT	R	R	RT	ST
R before S	S	SR	S	S	SR	TR
S before T	т	TS	т	т	TS	RS
cyclical	Е		Е	^E ext		
	RE	RsO, tRO	RE	RE	RE	ST
	SE	sto, rso	SE	SE	SE	TR
	TE	sTO, TrO	TE	TE	TE	RS
switch 1	RS	RS	RS	RS	SR	TR
(KZ91-1)	ST	ST	ST	ST	TS	RS
	TR	TR	TR	TR	RT	ST
1 ON	RSE	RSE	RSE	RSE	RE	ST
2 OFF	STE	STE	STE	STE	SE	TR
3 OFF	TRE	TRE	TRE	TRE	TE	RS
4 OFF	RST	RST	RST	RST	RT	ST
	RSTE	RSTE	RSTE	RSTE	RT	ST
R before S	R	RT	R	R	RT	ST
before T	S	SR	S	S	SR	TR
	т	TS	т	т	TS	RS
acyclical	E		E	Eext		
	RE	RsO, tRO	RE	RE	RE	ST
	SE	StO, rSO	SE	SE	SE	TR
switch 1	TE	sTO, TrO	TE	TE	TE	RS
(KZ91-1)	RS	RS	RS	RS	SR	TR
	ST	ST	ST	ST	TS	RS
1 ON	TR	TR	TR	TR	RT	ST
2 ON	RSE	RSE	RSE	RSE	RE	ST
3 ON	STE	STE	STE	STE	SE	TR
4 ON	TRE	TRE	TRE	TRE	RE	ST
	RST	RST	RST	RST	RT	ST
	RSTE	RSTE	RSTE	RSTE	RT	ST

Small letters: more distant cross-country fault
 Directional measurement: same current as distance meas.
 Table 6.3 - Continuation

F

Checking the direction of measurement

As soon as one or more of the test buttons (R, S or T) are pressed, the corresponding starting unit or units pick up and the auxiliary tripping relay is interlocked to prevent operating the circuit-breaker inadvertently.

The starting units apply phase quantities to the measuring unit according to the kind of fault being simulated.

The relay will pick up in some stage or other depending on the load current at the time and the relay settings. (If the values and phase-angle of the load quantities is known, it is even possible to test the various zones by suitably changing the settings.)

Providing the fourth zone is non-directional, the relay must at very least pick-up in the fourth zone (red LED lights up).

The fourth zone must be made directional and all the zones be measuring in the forwards direction in order to check the relay's ability to determine direction. The corresponding switch positions are: switch 8a in position 1, switch 8b in position 1 or 2 depending on input transformer connection and switch 7 in position 1.

Caution! Units may only be withdrawn from the rack when the auxiliary supply is switched off.

Under the above conditions the relay must pick up when the load energy is flowing in the forwards direction and block when the energy direction is reversed by operating switch 8b (part number HESG 439 686) or the frontplate switch $I \rightarrow$ (part number HESG 440 718), or vice versa.

The ability of the relay to pick up and block must be tested in this way for all kinds of faults.

Withdrawing a relay with non-directional fourth zone can be avoided by setting the thumbwheel switch N of the corresponding zone to 0% (infinite reach).

Should the direction be incorrect for a particular kind of fault, the polarity of all c.t's, p.t's, leads and connections must be checked (see Sections 5.3 and 5.4). If the direction is incorrect for all kinds of faults, all the current connections must be reversed. In the case of relays LZ92 and LZ92-1 with off-set starting characteristics care must be taken that the longest reach of the starters is in the desired direction (see Section 3.6).

Testing the tripping circuit

Provision is made on the input transformer unit EW91 from part number HESG 440 754 to apply a signal manually to the auxiliary tripping relay and thus trip the circuit-breaker. This operation, however, is interlocked for safety reasons. Firstly terminal E11 of the input transformer unit must be connected to terminal E63 of the main processing unit. Then the sockets \bigwedge and \neg must be connected. Finally a starting button must be pressed. It is not possible to test the tripping circuit, if the connection E11-E63 is not made.

6.6 Testing the distance relay using a test set

Providing the relay is equipped with a test socket XX91, a test set can be simply connected by means of the test connector YX91. As soon as the test connector is inserted, a signal is emitted (providing it is appropriately wired via an auxiliary signalling relay in the unit AV91 respectively AV94) to indicate that the relay is no longer standing by (V.O in the code of the main processing unit).

When it is plugged in the test connector:

- short-circuits the main c.t's
- isolates the relay input from c.t's and p.t's
- interrupts the tripping circuit
- blocks the external signals (driver outputs and signals to ancillaries, e.g. AV91, AV94 etc.) for starting, tripping and timer (where fitted) for the duration of testing
- connects the p.t. voltages to the test set (where necessary)
- connects the current and voltage inputs of the relay to the test set.

Facility is provided in the test connector for enabling the signal outputs for ancillary units if necessary.

A sample test report for testing with a test set can be found in the appendicies.

6.7 Instructions for modifying relay operation

Normally the desired relay operation is defined by the ordering code and supplied from the works with the corresponding connections etc., already made.

However, should changes become necessary during commissioning, then small changes (soldered link or value of a resistor on soldering posts) can be done with the aid of the Table of Features and corresponding PCB drawing in the appendicies. This work may only be performed by correspondingly qualified personnel.

The principal versions are defined as follows:

Plug-in unit EW91 (HESG 438 454)	Code	U1 U1			E7 E7					
Plug-in unit EW91 (HESG 440 754 resp. EW91a HESG 441 721)	Cođe	U1 (U1	A5 A1	K11		F5 F6)				
Plug-in unit KI91	Cođe	F5	11/0	0.5/	US	1.5 R20	X5	E00	FA1	v1 0
Plug-in unit KZ91	Cođe	F5	Z1 1	EK1 L	0 US	1.5 R20	X5	E00	FA1	V10
Plug-in unit KZ91-1	Code	F5	Z1 1	EK1 L	4 US	1.5 R21	X5	E00	FA1	V10

7. OPERATION AND MAINTENANCE

Where no remote reset is fitted, the LED signals must be reset each time the relay picks up (providing the signals are in operation, i.e. main processing unit code FA1). Please refer to Section 3.11 for main processing units with PCB 8 acc. to HESG 440 718 . The relay continues to operate correctly whether the signals are reset or not, but the signals may no longer be related to the fault concerned.

The relay requires no special maintenance. However, as is usual with all safety systems, it should be tested at regular intervals. This can be carried out as described in Section 6.5.

The test buttons on the frontplate permit a simple functional test to be carried out with the relay connected to load quantities. The latter can also be measured at the test sockets on the frontplate. Furthermore the tripping circuit including the circuit-breaker can be tested with input transformer units with part number HESG 440 754 resp. 441 721 and main processing unit with PCB 8 acc. to HESG 440 718.

Since the simple tests above can be carried out at relatively short intervals, comprehensive testing with a test set is only necessary at two-yearly intervals. But quantitive testing is essential after this period regardless of whether the relay is equipped with a test socket XX91 or not. Where it is not, either signals must be injected at the terminals after appropriately isolating the main c.t's and p.t's, or the plug-in units can be withdrawn and inserted into a separate equipment rack wired to the test set.

7.1 Ancillaries and spares

When ordering ancillaries and spares, it is essential that the type and serial number of the unit for which they are intended be stated. If a number of identical relays are installed in a plant, we recommend stocking spare units.

Spares must be stored in a clean dry room at moderate temperatures. It is further recommended that spares also be tested at the same time as the periodic testing of the protective equipment in operation, i.e. every one or two years.

8. TROUBLE-SHOOTING

Should the relay appear defective, proceed as follows:

- Check that the thumbwheel switch of the neutral current element in the KZ91 or KZ91-1 is not set to zero.
- Check the <u>+</u> 15 V and <u>+</u> 24 V auxiliary supplies are in order at the edge connector of each plug-in unit (see relative instructions for the terminal designations of the DC/DC converter).
- Check the correctness of the signals at the sockets on the front of the EW91 (see Section 6.3). If not, check the relay wiring.
- Localise a defect by withdrawing the KI or KZ unit and rechecking the signals at the sockets on the EW91. If they are in order, a defective main processing unit is to be suspected.
- Replace the defective unit.

Should it not be possible to locate and clear a defect, then the complete relay or plug-in unit concerned must be returned to the nearest BBC agent, or directly to ABB Baden, Switzerland.

9. APPENDICIES

Figure 9 Figure 9	.1 .2 .3 .4 .5 .6 .7 .8 .9 .10 .11 .12 .13 .14	Distance relay LZ91 Distance relay LZ92 Input transformer unit EW91, Part No. HESG 438 454 Input transformer unit EW91, Part No. HESG 440 754 Main processing unit KI91, Part No. HESG 440 079 R1R4 Main processing unit KZ91, Part No. HESG 440 080 R11R14, R22 Main processing unit KZ91-1, Part No. HESG 440 342 R11, R12, R22 Wiring diagram
Table 9 Table 9	.15 .16 .17 .18	Features of plug-in unit EW91 Features of plug-in unit KI91 Features of plug-in unit KZ91 Features of plug-in unit KZ91-1
Figure 9 Figure 9	.19 .20 .21 .22 .23 .24 .25 .26 .27 .28 .29 .30 .31 .32 .33 .34	<pre>PCB 1 in EW91 (HESG 438 048) PCB 1 in EW91 (HESG 440 752) PCB 1 in KI91 (HESG 439 679) PCB 1 in KZ91 (HESG 439 689) PCB 1 in KZ91-1 (HESG 440 363) PCB 2 in KI91 (HESG 439 681) PCB 2 in KZ91, KZ91-1 (HESG 439 691) PCB 3 in KZ91, KZ91-1 (HESG 439 692) PCB 5 in KI91, KZ91, KZ91-1 (HESG 439 683) PCB 6 in KI91, KZ91, KZ91-1 (HESG 439 683) PCB 7 in KI91, KZ91, KZ91-1 (HESG 439 684) PCB 8 in KI91, KZ91, KZ91-1 (HESG 439 685) PCB 8 in KI91, KZ91, KZ91-1 (HESG 439 686) PCB 8 in KI91, KZ91, KZ91-1 (HESG 440 718) PCB 8 (soldered side) in KI91, KZ91, KZ91-1 (HESG 440 719) Sample test report for distance relay LZ91</pre>

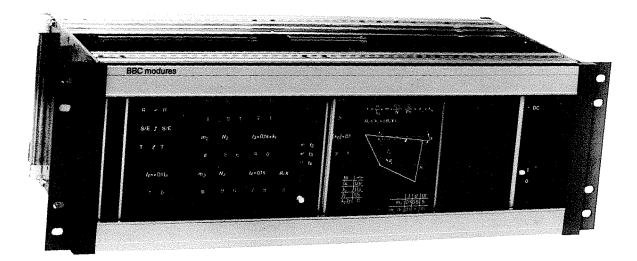


Fig. 9.1 - Distance relay LZ91 with plug-in units KI91, EW91 and NF92

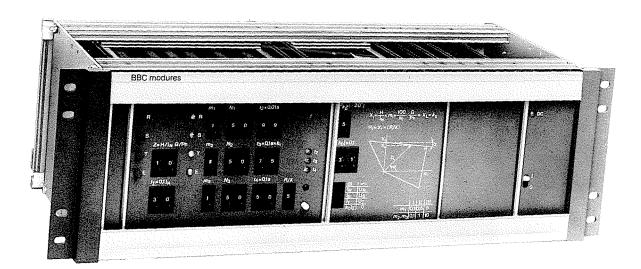


Fig. 9.2 - Distance relay LZ92 with plug-in units KZ91, EW91 and NF92

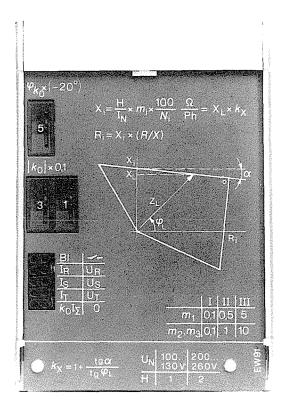


Fig. 9.3 - Input transformer unit EW91, Part No. HESG 438 454 (photo 185 680)

(

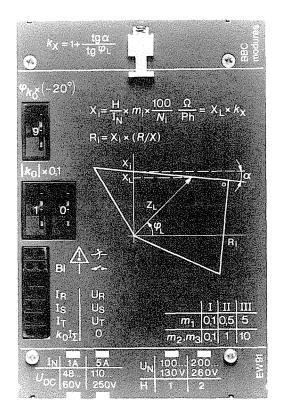


Fig. 9.4 - Input transformer unit EW91, Part No. HESG 440 754 (photo 214 280)

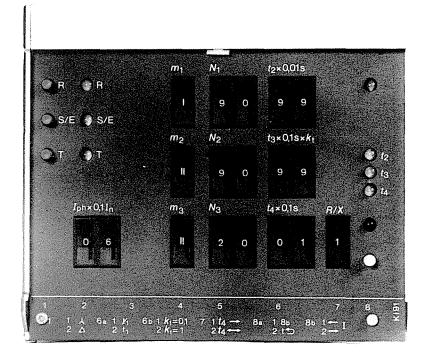


Fig. 9.5 - Main processing unit KI91, Part No. HESG 440 079 R1...R4 (photo 188 349)

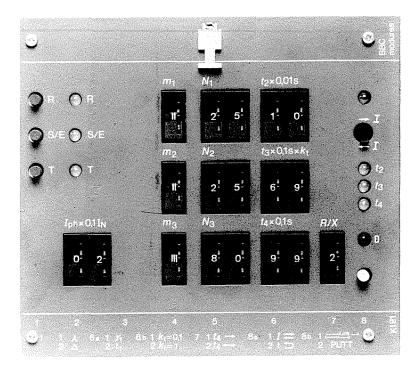


Fig. 9.6 - Main processing unit KI91, Part No. HESG 440 079 R11...R14 (photo 214 281)

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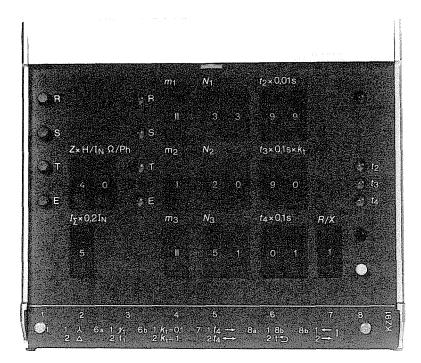


Fig. 9.7 - Main processing unit KZ91, Part No. HESG 440 080 R11...R14, R23 (photo 188 350)

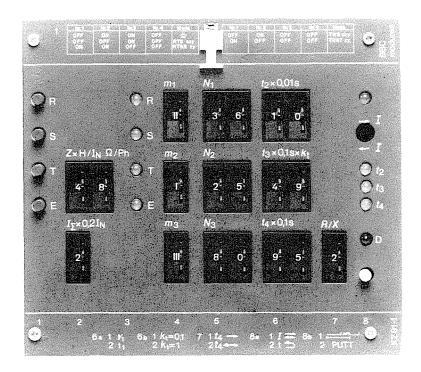
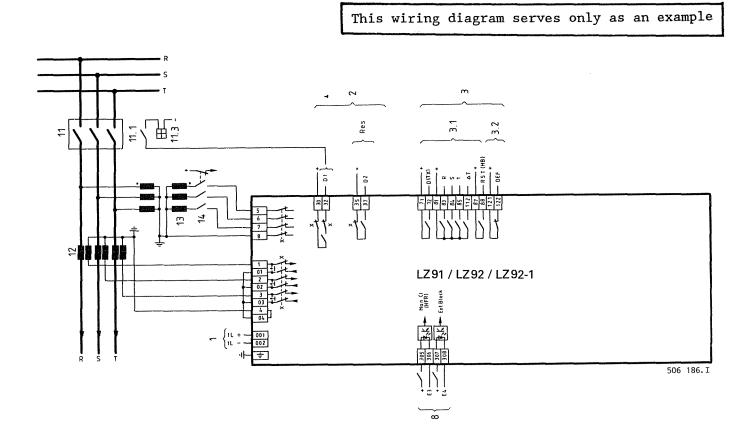


Fig. 9.8 - Main processing unit KZ91-1, Part No. HESG 440 342 R11, R12, R22 (photo 214 279)



1	=	aux. supply from station battery
2	=	tripping contacts
3	=	signalling contacts
		3.1 = operating signals
		3.2 = relay defect
8	=	opto-coupler inputs
11	=	circuit-breaker
		11.1 = circuit-breaker aux. contact
		11.3 = tripping coil
12	=	c.t's
13	=	p.t's
14	=	m.c.b. with aux. contact

Fig. 9.9 - Wiring diagram

45

Code	Feature	Spe	ecification/Component change
U1 U2	Rated voltage 100 to 130 V a.c. Rated voltage 200 to 260 V a.c.	> der	pending on component ratings
A1 A5 A2	Rated current 1 A a.c. Rated current 5 A a.c. Rated current 2 A a.c.	chc HES	5G 438 454 Dice of terminals (see Sec. 5.3) 5G 438 454 acc. to components tted
A1 A2 A5	Rated current 1 A a.c. Rated current 2 A a.c. Rated current 5 A a.c.	> de <u>r</u>	5G 440 754 resp. HESG 441 721 pending on component ratings d calibration on PCB 1
F5/F6 F5/F6	Rated frequency 50 Hz/60 Hz Rated frequency 50 Hz/60 Hz	W11 HES	5G 438 454 15 and R44, R45, R46 and Ei 5G 440 754 resp. HESG 441 721) 5, W16, W17, W18 and Ei
K.O K.1 K1. K0.	k amplitude k amplitude and phase-angle with tripping unit without tripping unit	> HES	SG 438 454
к11	k amplitude and phase-angle	HES	SG 440 754 resp. HESG 441 721
E0 to E8	Opto-coupler input voltage		SG 438 454 5, R26
E11 to E13	Opto-coupler input voltage		SG 440 754 resp. HESG 441 721 44, R146
E1 to E4 E5 to E8	Opto-coupler input Q Opto-coupler input \overline{Q}	HES W63	SG 438 454 1
	Opto-coupler input Q, Q	HES W61	SG 440 754 resp. HESG 441 721 1

Ei works calibration (module No.)

LB. soldered link No.

L0 PCB lay-out change at the works (different PCB)

Wi resistor No.

Table 9.15 - Features of plug-in unit EW91

Code	Feature	Specification/Component change	
F5 F6	Rated frequency 50 Hz Rated frequency 60 Hz	PCB 6 (HESG 439 684) LB1 PCB 7 (HESG 439 685) Ei(M8	35)
11/0.5 12/0.5 1.//	Standard selection logic TRS selection logic $I_{\Sigma}/I_{Ph} \neq 0.5$	PCB 1 (HESG 439 679)L0PCB 1 (HESG 439 680)L0PCB 2 (HESG 439 681)Wi2	
US1.5 US	Overreach 1.5 x Z (standard) Overreach \neq 1.5 x ¹ Z ₁	<pre>PCB 5 (HESG 439 683) Wi8</pre>	
R10 R20 R30 R40	Reverse direction zone 1 Reverse direction zone 2 Reverse direction zone 3 Reverse direction zone 4	PCB 6 (HESG 439 684) LB9	
R50	Reverse direction external	PCB 6 LB9 and PCB 8 LB17 (HESG 439 686) or PCB 8 (HESG 440 718) LB17	
x5 x	Reactance inclination 5° Reactance inclination \neq 5°	PCB 7 (HESG 439 685) Ei(M8 PCB 7 (HESG 439 685) Ei(M8	•
EO. E1. E.O E.1	Zone 4 less reactance Zone 4 plus reactance Zone 4 less resistance Zone 4 plus resistance	PCB 7 (HESG 439 685) LB11 LB12 LB12	
FA1 FA2	Frontplate signals operational Frontplate signals non-opera- tional	PCB 2 (HESG 439 681) LB13 PCB 8 (HESG 439 686) ot LB15 PCB 8 (HESG 440 718) LB15	
V10 V11	With defect signalling during testing via test socket Without defect signalling during testing via test socket	PCB 8 (HESG 439 681) orLB16PCB 8 (HESG 440 718)LB16PCB 8 (HESG 439 681) orLB16PCB 8 (HESG 440 718)LB16	

- works calibration (module No.) Ei
- LB
- soldered or plug-in link works PCB-layout (different PCB) LO
- Wi resistor No.

Table 9.16 - Features of plug-in unit KI91

Code	Feature	Specification/Component change
F5 F6	Rated frequency 50 Hz Rated frequency 60 Hz	PCB 6 (HESG 439 684) LB1 PCB 7 (HESG 439 685) Ei(M85)
Z1/ Z2/	Starting char. circle Starting char. lense 108°	PCB 3 (HESG 439 692) LB3
Z.// Z.// Z.//// Z.////	Starting char. about origin Starting char. off-set E/F compensation $(1+k)/2 = 1$ E/F compensation $(1+k_0)/2 \neq 1$	> PCB 3 (HESG 439 692) Wi4 PCB 2 (HESG 439 691) Wi5
EK1 EK2	E/F logic OR E/F logic AND	<pre>> PCB 2 (HESG 439 691) LB6 </pre>
L0 L1 L2	RTS selection logic (standard) Cycl. RTSR selection logic TRS selection logic	PCB 1 (HESG 439 691) LB7
L3	Cycl. TRST selection logic	PCB 1 (HESG 439 690) L0 and LB7
US1.5 US	Overreach 1.5 x Z ₁ (standard) Overreach \neq 1.5 x Z ₁	PCB 5 (HESG 439 683) Wi8
R10 R20 R30 R40	Reverse direction zone 1 Reverse direction zone 2 Reverse direction zone 3 Reverse direction zone 4	> PCB 6 (HESG 439 684) LB9
R50	Reverse direction external) PCB 6 LB9 and PCB 8 LB17 (HESG 439 686) or
		PCB 8 (HESG 440 718) LB17
x5 x	Reactance inclination 5° Reactance inclination ≠ 5°	PCB 7 (HESG 439 685) Ei(M85) PCB 7 (HESG 439 685) Ei(M85)
E0. E1. E.0 E.1	Zone 4 less reactance Zone 4 plus reactance Zone 4 less resistance Zone 4 plus resistance	LB11 PCB 7 (HESG 439 685) LB11 LB12 LB12 LB12
FA1 FA2	Frontplate signals operational Frontplate signals non-opera- tional	PCB 3 (HESG 439 692) LB14 PCB 8 (HESG 439 686) or LB15 PCB 8 (HESG 440 718) LB15
V10 V11	With defect signalling during testing via test socket Without defect signalling during testing via test socket	PCB 8 (HESG 439 681) or LB16 PCB 8 (HESG 440 718) LB16 PCB 8 (HESG 439 681) or LB16 PCB 8 (HESG 440 718) LB16 PCB 8 (HESG 440 718) LB16

Ei works calibration (module No.)

LB soldered or plug-in link

LO works PCB-layout (different PCB)

Wi resistor No.

Table 9.17 - Features of plug-in unit KZ91

Code	Feature	Specification/Component change
F5 F6	Rated frequency 50 Hz Rated frequency 60 Hz	PCB 6 (HESG 439 684) LB1 PCB 7 (HESG 439 685) Ei (M85)
Z1/ Z2/	Starting char. circle Starting char. lense 108°	<pre>PCB 3 (HESG 439 692) LB3</pre>
Z.// Z.// Z.//// Z.////	Starting char. about origin Starting char. off-set E/F compensation $(1+k)/2 = 1$ E/F compensation $(1+k_0^0)/2 \neq 1$	PCB 3 (HESG 439 692) Wi4 PCB 2 (HESG 439 691) Wi5
EK1 EK2	E/F logic OR E/F logic AND	PCB 2 (HESG 439 691) LB6
L4	Selection logic	PCB 1 (HESG 440 363) switch 1
US1.5 US	Overreach 1.5 x Z (standard) Overreach \neq 1.5 x ¹ Z ₁	<pre>PCB 5 (HESG 439 683) Wi8</pre>
R11 R21 R31 R41	Reverse direction zone 1 Reverse direction zone 2 Reverse direction zone 3 Reverse direction zone 4	PCB 6 (HESG 439 684) LB9
R51	Reverse direction external	PCB 6 LB9 and PCB 8 LB17 (HESG 439 686) or PCB 8 (HESG 440 718) LB17
X5 X	Reactance inclination 5° Reactance inclination \neq 5°	PCB 7 (HESG 439 685) Ei(M85) PCB 7 (HESG 439 685) Ei(M85)
EO. El. E.O E.1	Zone 4 less reactance Zone 4 plus reactance Zone 4 less resistance Zone 4 plus resistance	PCB 7 (HESG 439 685) LB11 LB12 LB12
FA1 FA2	Frontplate signals operational Frontplate signals non-opera- tional	PCB 3 (HESG 439 692) LB14 PCB 8 (HESG 439 686) or LB15 PCB 8 (HESG 440 718) LB15
V10 V11	With defect signalling during testing via test socket Without defect signalling during testing via test socket	PCB 8 (HESG 439 681) or LB16 PCB 8 (HESG 440 718) LB16 PCB 8 (HESG 439 681) or LB16 PCB 8 (HESG 440 718) LB16 PCB 8 (HESG 440 718) LB16

Ei works calibration (module No.)

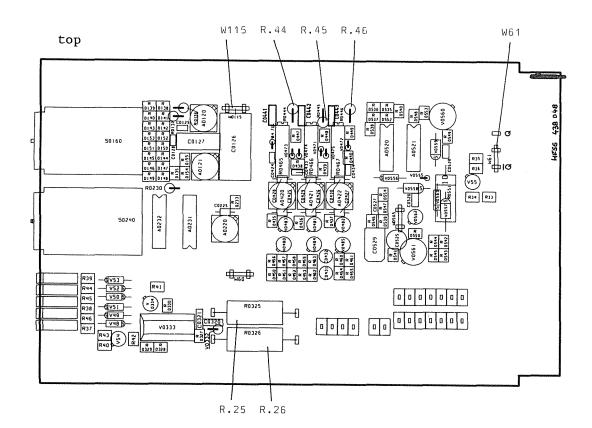
LB soldered or plug-in link

LO works PCB-layout (different PCB)

Wi resistor No.

Table 9.18 - Features of plug-in unit KZ91-1

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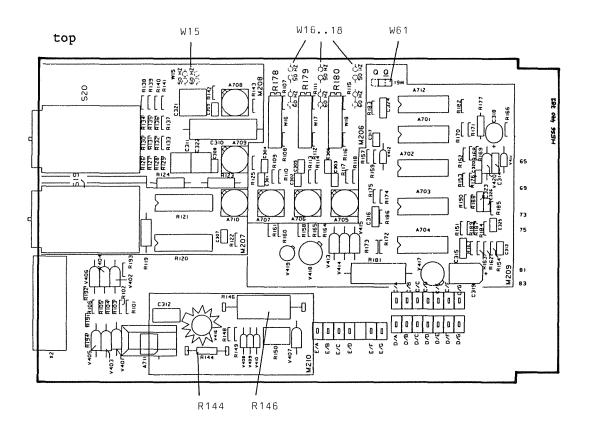


W115 Rated frequency 50 Hz, code F5: o--o (amplitude calibration also 60 Hz, code F6: -o o- necessary)
R.44 Rated frequency 50 Hz, code F5: R.44, R.45, R.46 = 5.6 kOhm
R.45 Rated frequency 60 Hz, code F6: R.44, R.45, R.46 = 3.9 kOhm
R.46
W61 direct opto-coupler input, code E1...E4, Q: o o--o

W61direct opto-coupler input , code E1...E4, Q: o o--oinverted opto-coupler input , code E5...E8, Q: o--o o

R.25 opto-coupler input R.26 voltage

Code	Voltage	R.25 = R.26				
E1, E5 E2, E6 E3, E7 E4, E8 E0	24 V 48 60 V 110125 V 220250 V	1,5 k 2 W 3,3 k 2 W 6,8 k 2 W 18 k 2 W CO and remove link W61				



W15...W18Rated frequency 50 Hz, code F5state link posi-
tions on PCB(amplitude calibration
also necessary)

 $\frac{W61}{M} \qquad \begin{array}{c} \text{direct opto-coupler output, without code } \underline{Q}: (o--o) \text{ o} \\ \text{inverted opto-coupler output, without code } \overline{Q}: o \quad (o--o) \end{array}$

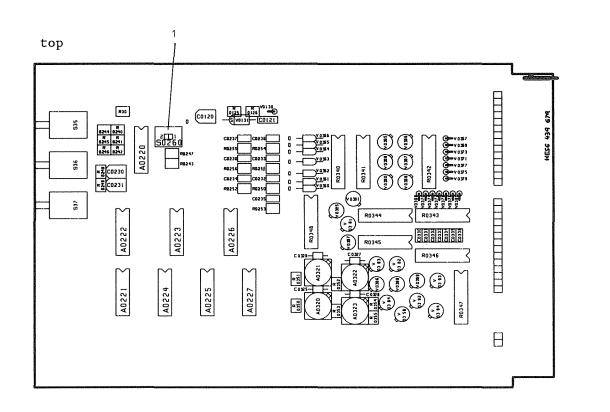
R144 R146	opto-coupler input voltage	Code	Voltage	R144	R146
		E11 E12 E13	20 30 V 36 75 V 82312 V	1 k 2W 2.7k 2W 15 k 2W	4.7k 2W 27 k 2W 82 k 2W

PCB 1, unit EW91a: The arrangements of the elements on this
(HESG 441 585) PCB have been slightly modified. The above mentioned
jumpers and resistors are in the same spot as in the
EW91.

Fig. 9.20 - PCB 1 in EW91 (HESG 440 752)

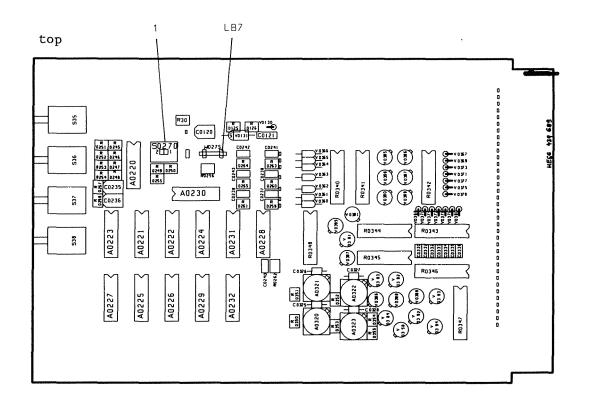
51

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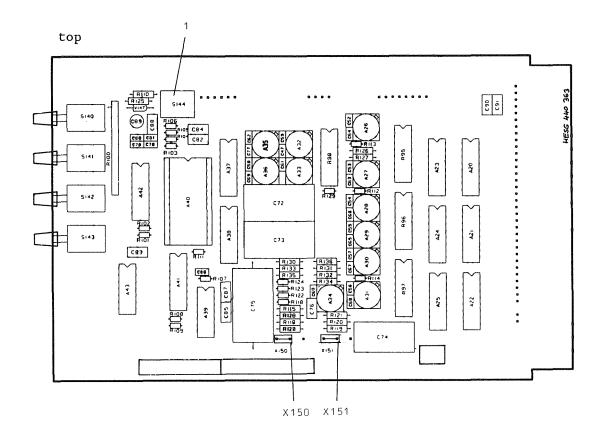
1 Switch 1: position 1 for mode \checkmark position 2 for mode \bigtriangleup

Fig. 9.21 - PCB 1 in KI91 (HESG 439 679/80)



- 1 Switch 1: position 1 for mode \land position 2 for mode \bigtriangleup

Fig. 9.22 - PCB 1 in KZ91 (HESG 439 689/90)



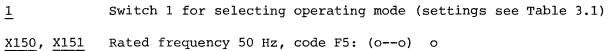
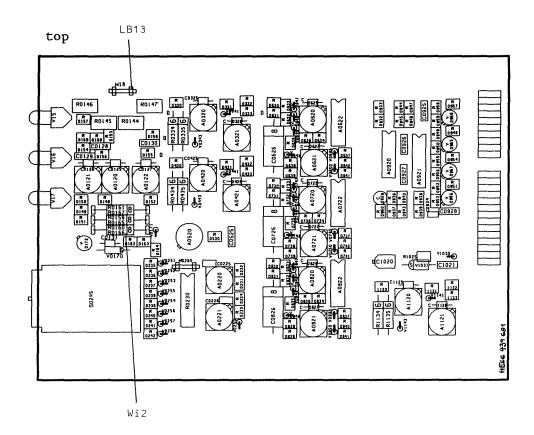


Fig. 9.23 - PCB 1 in KZ91-1 (HESG 440 363)

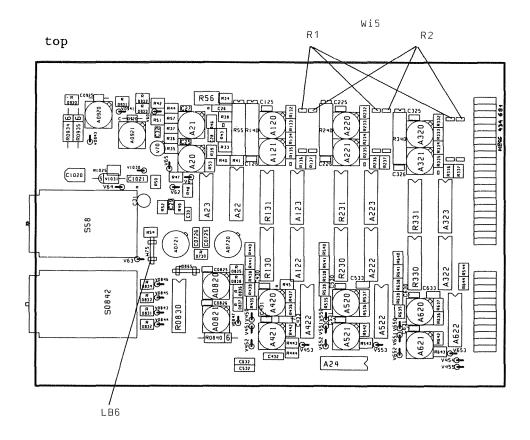


LB13 Frontplate signals operational, code FA1: o--o Frontplate signals non-operational, code FA2: -o o-

<u>Wi2</u>	I_{Σ}/I_{Ph}	R65	R59	R67	R61	R66	R60	
	1	ω	ω	ω	ω	ω	ω	
	0.76	150k	∞	150k	∞	150k	ω	
	0.5 *	ω	47k	ω	47k	ω	47k	
	0.33	47k	47k	47k	47k	47k	47k	
				-				

* standard

Fig. 9.24 - PCB 2 in KI91 (HESG 439 681)

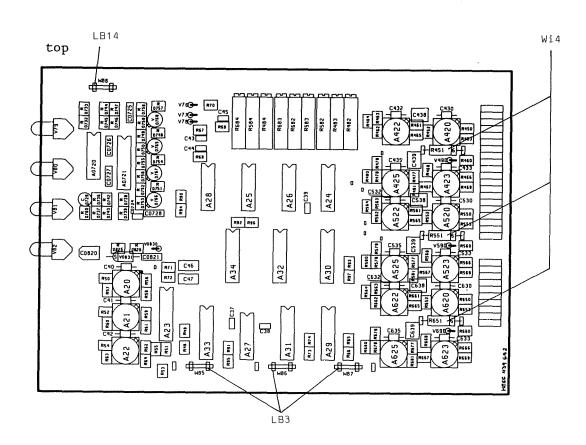


 $\begin{array}{c} \underline{\text{LB6}} & \text{E/F logic OR, code EK1: } & \text{o--o} & (\text{I}_{\Sigma} \text{ OR } \text{U}_{\Sigma}) \\ & \text{E/F logic AND, code EK2: -o} & \text{o-} & (\text{I}_{\Sigma} \text{ AND } \text{U}_{\Sigma}) \end{array} \end{array}$

<u>Wi5</u> E/F compensation $(1+k_0)/2$, code Z.///.../: see table for R1 and R2

$k_{o} \leq 1$				k _o ≥ 1							
ko	<u>1+k</u> <u>o</u> 2	R1	R2 (kΩ)	k o	 2	R1 (kΩ)	R2	k o	<u>1+k</u> 2	R1 (kΩ)	R2
1 0.82 0.79 0.75 0.71 0.66 0.61 0.55 0.48 0.40 0.31 0.23 0.12 0.03	1 0.91 0,895 0.875 0.855 0.83 0.805 0.775 0.74 0.70 0.655 0.615 0.56 0.515	o p e n	∞ 1000 820 680 560 470 390 330 270 220 180 150 120 100	1 1.09 1.11 1.13 1.16 1.20 1.24 1.28 1.34 1.42 1.52 1.62 1.78 1.94	1 1.045 1.055 1.065 1.08 1.10 1.12 1.14 1.17 1.21 1.26 1.31 1.39 1.47	<pre></pre>	8 0 p e n 8	2.14 2.38 2.67 3.00 3.41 3.84 4.48 5.27	1.57 1.69 1.835 2.0 2.205 2.42 2.74 3.135	82 68 56 47 39 33 27 22	CO of f e n CO

Fig. 9.25 - PCB 2 in KZ91 and KZ91-1 (HESG 439 691)



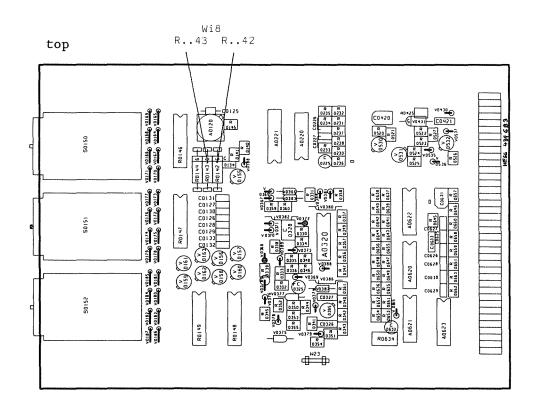
- LB3 Starting char.: circle, code Z1/: o o--o o o--o o Starting char.: lense 108°, code Z2/: o--o o o--o o o o--o
- Wi4Starting char. about origin, code Z./1/R..5147 kStarting char. off-set,code Z./xy/R..51see table

ху	0.26	0.39	0.57	0.69	0.84	1	2.14	3.13	3.91
R51 (kΩ)	180	120	82	68 ·	56	47	22	15	12

Resistor rating: 0.25 W

LB14 Frontplate signals operational, code FA1: o--o Frontplate signals non-operational, code FA2: -o o-

Fig. 9.26 - PCB 3 in KZ91 and KZ91-1 (HESG 439 692)

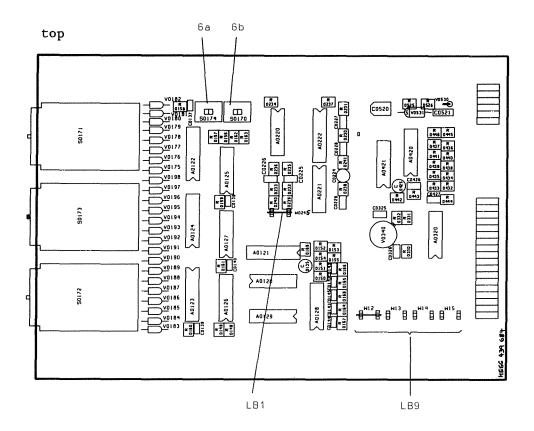


z _{us} /z ₁	R42 (kOhm) (1%)	R43 (kOhm) (1%)	z _{us} /z ₁	R42 (kOhm) (1%)	R43 (kOhm) (1%)
1	ω	ω	2	4.75	0
1.05	47.5	47.5	2.25	3.32	0.475
1.1	47.5	0	2.5	2.21	1
1.15	27.4	3.92	2.75	1.5	1.21
1.2	18.2	5.62	3	1.82	0.562
1.25	18.2	0.825	3.25	1.82	0.274
1.3	15	0.825	3.5	1.82	0.0825
1.35	12.1	1.5	3.75	1.5	0.221
1.4	10	1.82	4	1.5	0.0825
1.45	8.25	2.21	4.25	1	0.475
1.5	* 4.75	* 4.75	4.5	1.21	0.15
1.6	5.62	2.21	4.75	1.21	0.0562
1.7	3.92	2.74	5	1	0.182
1.8	5.62	0.332			
1.9	4.75	0.562			

* standard

(note: R..44 is always 4.75 kOhm)

Fig. 9.27 - PCB 5 in KI91, KZ91, KZ91-1 (HESG 439 683)



<u>6a</u>	Switch	6a:	position position	1 2	≠1 t1	: :	zone zone	1 1	blocked operational		
<u>6b</u>	Switch	6b:	position position			: :	zone zone	3 3	timer multiplier k _t = timer multiplier k _t =	0.1	L

LB1 Rated frequency 50 Hz, code F5: soldered link LB1 closed Rated frequency 60 Hz, code F6: soldered link BL1 open

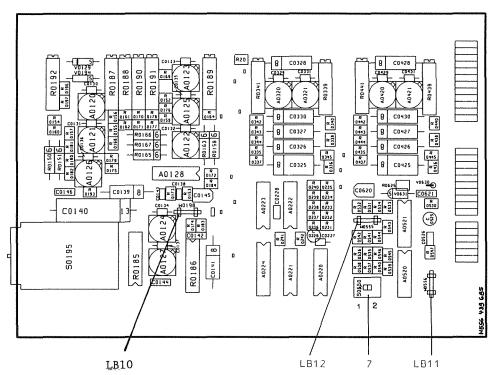
code	W12	W13	W14	W15
R2. R1. R3. R4. R5.	00 0 0 0 0 0 0	0 0 00 0 0 0 0 0 0	0 0 0 0 00 0 0 0 0	0 0 0 0 00 0 0
zone	t ₂	t ₁	t ₃	t ₄

LB9 One zone with reversed direction (see table)

Fig. 9.28 - PCB 6 in KI91, KZ91, KZ91-1 (HESG 439 684)

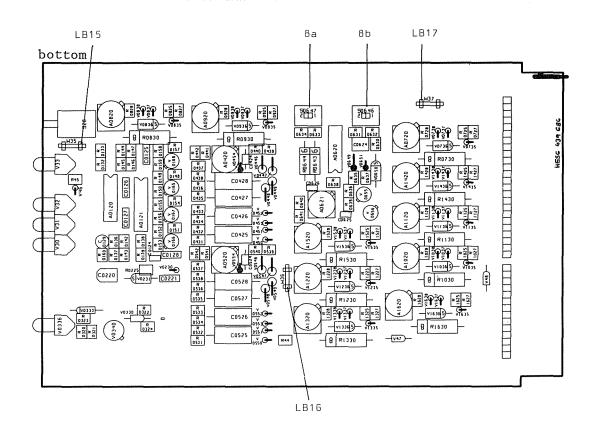
59

top



- LB11Zone 4 less reactance characteristic, code E0. :o--oZone 4 plus reactance characteristic, code E1. :-o
- <u>LB12</u> Zone 4 less resistance characteristic, code EO. : o--o Zone 4 plus resistance characteristic, code El. : -o o-
- <u>LB10</u> Reference voltage = fault voltage, without code: o--o o (for KI91, KZ91) Reference voltage = fault voltage, without code: o o--o (for KZ91-1)

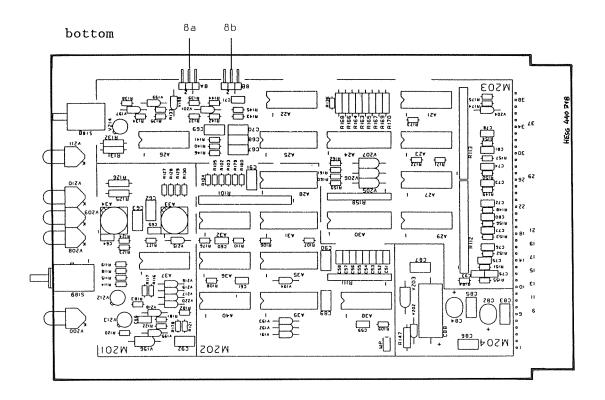
Fig. 9.29 - PCB 7 in KI91, KZ91, KZ91-1 (HESG 439 685)



Switch 8a: position 1 : direction as given on 8b 8a 8b position 2 t 🗇 : direction as given on 8b, but one zone reserved I ← : direction reversed 8b Switch 8b: position 1 position 2 I -> : forwards direction into the line (if wired according to wiring diagram) LB15Frontplate signals operational, 0--0 :

- LB15 Frontplate signals operational, code FA1 : o--o Frontplate signals non-operational, code FA2: -o o-
- LB16With defect signalling during
testing via test socket, code V10: o--oWithout defect signalling during
testing via test socket, code V11: -o o-
- LB17 Reversal of direction by external signal, code R5 : o--o The position of LB17 is given for codes R1. to R4. and correspondingly soldered at the works.

Fig. 9.30 - PCB 8 in KI91, KZ91, KZ91-1 (HESG 439 686)



8a Plug-in link 8a: position 1 I ↔ : o (o--o) position 2 t ↔ : (o--o) o

Plug-in link 8b: position 1 $\xrightarrow{---}$: o (o--o) over

position 2 PUTT : (o--o) o

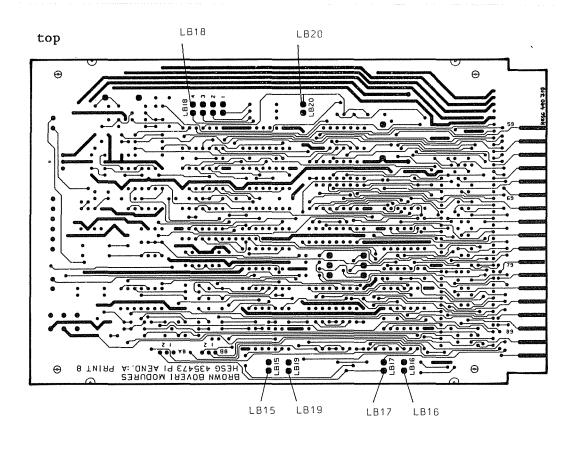
direction as set on frontplate switch I $\stackrel{\longrightarrow}{\longleftarrow}$ direction as set on frontplate switch I $\stackrel{\longrightarrow}{\longleftarrow}$ but one zone reversed

overreach (acceleration scheme) permissive underreaching transfer tripping (providing the scheme is wired)

LB15...LB20 see Fig. 9.32

Fig. 9.31 - PCB 8 in KI91, KZ91, KZ91-1 (HESG 440 718)

8b



LB15	Frontplate signals (LED's on PCB 8) operational Frontplate signals (LED's on PCB 8) non-operational	code code				
<u>LB16</u>	With defect signalling during testing via test socket, Without defect signalling during testing via test socket,	code code				
<u>LB17</u>	Reversal of direction by external signal,	code	R5.	:	0-	-0
<u>LB18</u>	Soldered links 1 to 4 open: no intentional zone 1 dela One or more of the soldered links 1 to 4 closed: Zone 1 delayed by the sum of the weightings times the system period (T_N) link 1: link 2: link 3: link 4:	power	r	: : :	0- 0- 0-	-0 -0
<u>LB19</u>	Signals maintained until reset Signals only maintained if relay trips			:	-0 0-	0- -0
<u>LB20</u>	Directional measurement taken into account when operation a PUTT scheme (plug-in link 8b in position 2 PUTT)	ing		:	-0	-0-

Fig. 9.32 - PCB 8 (soldered side) in KI91, KZ91, KZ91-1 (HESG 440 719)

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Test Report for Distance Relay Type LZ91

Station:				Line:		· · · · · · · · · · · · · · · · · · ·		Relay No.:					Grading table dated:		
Code No.	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			((-20°) =	[A]	$X_1 = \dots$			[Ω/Ph] t ₂ = [Ω/Ph] t ₃ =						
Relay test	1	Start	ting units		Mea	suring unit	[Ω/Ph]						PCB settings		
Fault	I _{Phase} specified			[A] ★ measured		zone measured		zone measured		zone measured	overr specified		1 : Mode		
R - 0 S - 0 T - 0 R - T S - R T - S													$6a : 1 \ t_{f} = 0,1$ $6b : 1 \ k_{f} = 0,1$ $7 : 1 \ t_{4} \longrightarrow$ $8a : 1 \ 8b$ $8b : 1 \longrightarrow I$ at HESG 440 $8a : 1 \ I \implies$	2 \triangle Selection logic $\begin{vmatrix} 2 & t_1 \\ 0 & 2 & t_1 \end{vmatrix}$ $\begin{vmatrix} 2 & k_1 = 1 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & t_4 = 0 \\ 0 & 2 & 0 & 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 = 0 \\ 0 & 2 & 0 & 0 \\ 0 & 2 & 0 & 0 \\ 0 & 2 & 0 & 0 \\ 0 & 0 & 0 & 0 \end{vmatrix}$ $\begin{vmatrix} 2 & t_4 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 &$	
-	Time step Test impo		T	perating tin zone	ne <u>† [s]</u> 2nd	zone	3rd z	one	4th	zone	Överre	each i	Overreach:		
		[\[] \[] \[]		·····		1		measured		r		T		• 🔲, other angles	
R - 0 T - S													* Only for mode $~\Delta$, -	
Remarks: 83-01-21 Lo. Ju SK - HESG 5001					Circuit-breaker: V.t's: C.t's:						Ancillary equipment: Tested by: Date: Signed:			Date:	

	ERI	5	> .			Test	Report	for Distan	ce Relay	Type LZ	.92)	LZ92-1	
Station:				Line:				Relay	No.:				Grading table dated:	
Code No.	EW91				KZ91-1									<u></u>
													$I_{\Sigma} \times 0,2 I_{N} = \dots$	
	•			•				•					t ₁ =	
	m ₂ =	•••••••		-									t ₂ =	
	-	,1 =			(-20°) =								†3 = †4 =	
Relay test	1	Starting un	iits		leasuring u								PCB settings	
Fault	sp	ecified	measured	1st	zone	2nd	zone	3rd	zone	Overr	each		1 : Mode (LZ92)	an a
	[•]	[Ω/Ph]	<u>[Ω/Ph]</u>	specified	measured	specified	measured	specified	measured	specified	measured		1人口,2人	Selection logic
R – 0													1 : Mode (LZ92-1)	Switch
													Nr. 1 Nr. 2 Nr. 3	Nr. 6 Mode
S - 0													$6a:1t/\Box$. 2 t 1
T - 0						-							$6a : 1 \not \downarrow \square$ $6b : 1 k_{\uparrow} = 0,1 \square$	$2 k_{f} = 1$
													$7 : 1 \uparrow_4 \longrightarrow \square$	
R – T						•								, 2 † 🗂 🗌 Zone
S – R													8b : 1 - I	, 2 → I
		<u> </u>											bzw. ab HESG 440718 8a : 1 I ≠ □	, 2 + D Zone
T - S													8b : 1 ===- 🗌	
	Time ste		T		me t [s]								Impedance starting:	
	Test imp			zone		zone	<u> </u>	zone measured		zone	Overr specified			, with off-set $X_A / X_B = \dots$
	[•]		shectlied	measured	specified	measured	specified	ineasured	specified	measured	specified	ineasureu	Factor $\frac{1+k_0}{2}$	
R – 0													2 Overreach:	
T - S													Reactance slope 5°	·
Remarks:		<u>I</u>		.	Circuit-breaker:						Ancillary equipment:			ested by:
					V.t's:							Date:		
83-01-20 ග	Lo. <	en SK -	HESG 500)2	C.t's:		<u></u>						Si	gned:

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Our experience has shown that, if the information and recommendations contained in these "Instructions for Installation and Operation" are observed, the best possible reliability of our products is assured.

It is scarcely possible for the operating instructions of technical equipment to cover every eventuality which can occur in practice. We would therefore request you to notify us or our agent in the case of all unusual behaviour, which does not appear to be covered by these operating instructions.

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